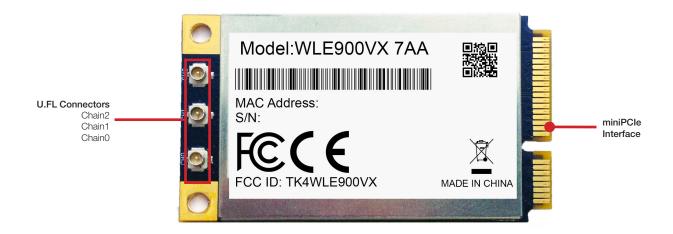
HARDWARE GUIDE - WLE900VX

Component Map



GPIO Pin Mapping

GPIO Pin	Function	GPIO Pin	Function
GPIO_0	WLAN_DISABLE_L	GPIO_1	LED_WLAN_L
GPIO_2	MCI_CLK_IN	GPIO_3	MCI_CLK_OUT
GPIO_4	MCI_DATA_OUT	GPIO_5	MCI_DATA_IN
GPIO_19	FEM_VRX	-	

Power Requirements

DC Power	3.3V
Power Consumption	4.7 Watts (Max)

miniPCle Slot Pin Assignment

Top side		Bottom side			
1	WAKE_L	2	+3.3V		
3	NC	4	GND		
5	NC	6	NC		
7	CLKREQ_L	8	MCI_CLK_IN (RESERVED)		
9	GND	10	MCI_CLK_OUT (RESERVED)		
11	REFCLK-	12	MCI_DATA_OUT (RESERVED)		
13	REFCLK+	14	MCI_DATA_IN (RESERVED)		
15	GND	16	NC		
Mechanical key					
17	NC	18	GND		
19	NC	20	W_DISABLE_L		
21	GND	22	RESET		
23	PERn0	24	+3.3V		

Top side		Bottom side	
PERp0	26	GND	
GND	28	NC	
GND	30	NC	
PETn0	32	NC	
PETp0	34	GND	
GND	36	NC	
GND	38	NC	
+3.3V (RESERVED)	40	GND	
+3.3V (RESERVED)	42	NC	
GND	44	LED_WLAN_L	
NC	46	BT_LED	
test point	48	NC	
NC	50	GND	
NC	52	+3.3V	
	GND GND PETn0 PETp0 GND GND +3.3V (RESERVED) +3.3V (RESERVED) GND NC test point NC	GND 28 GND 30 PETn0 32 PETp0 34 GND 36 GND 38 +3.3V (RESERVED) 40 +3.3V (RESERVED) 42 GND 44 NC 46 test point 48 NC 50	

