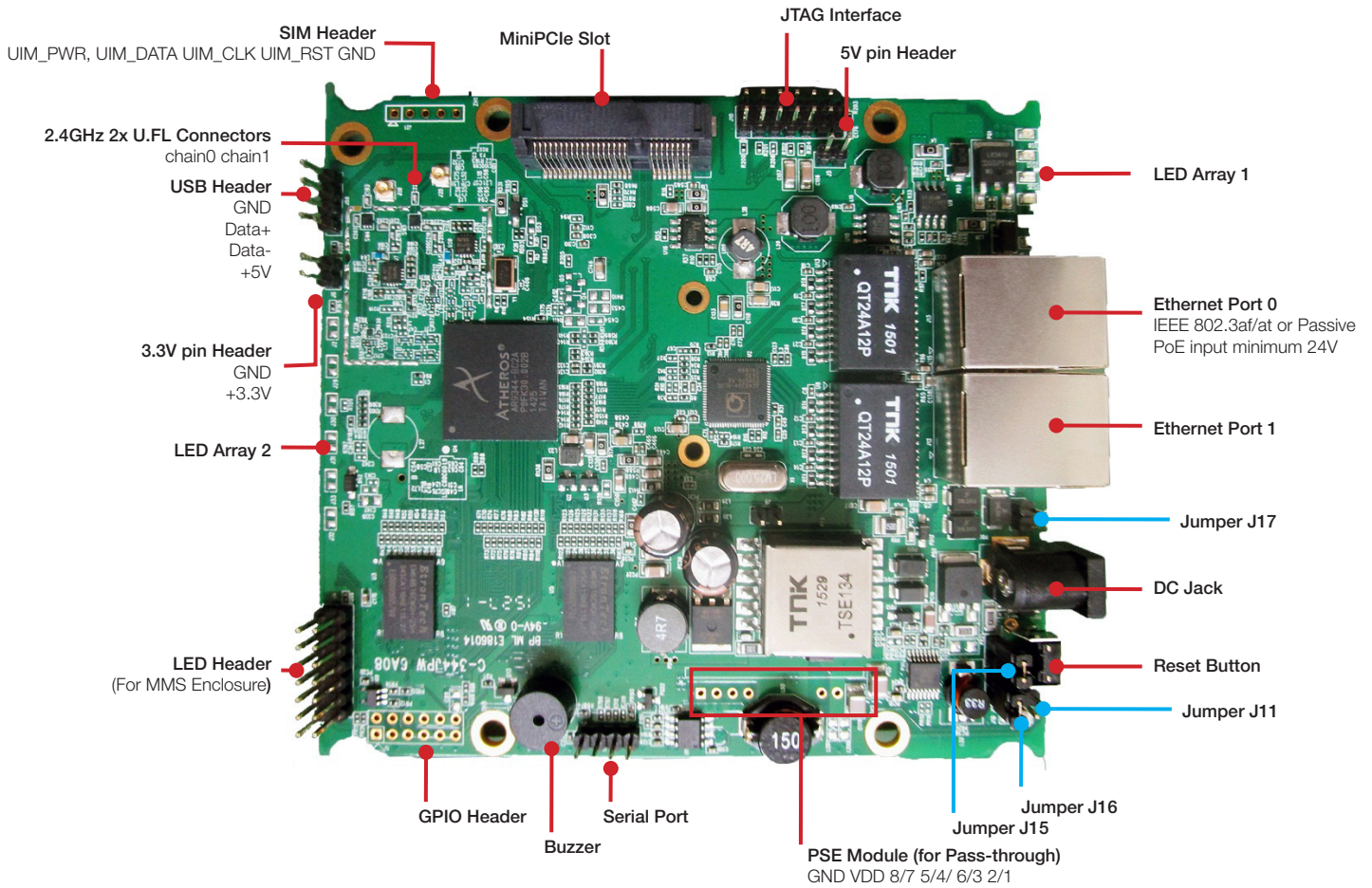


Component Map



Power Requirements

Power Over Ethernet	1x IEEE 802.3at or Passive PoE input minimum 24V
DC Power	1x DC Jack Connector: 24–56V for HV version 12–24V for LV version
Power Consumption	7 Watts (Max)

Voltage Jumper Configuration

Mode	J11	J15	J16	J17
Low Voltage PoE	OFF	ON	ON	OFF
IEEE 802.3af PoE	ON	OFF	OFF	ON

Ethernet 0 PINOUT

Pin	Signal
1	TX+/POE+
2	TX-/POE+
3	RX+/POE-
4	TX+/POE+
5	TX-/POE+
6	RX-/POE-
7	RX+/POE-
8	RX-/POE-

Serial Port Pin Assignment

Pin	Signal
1	VCC – 3.3V
2	UART 0 Transmit Data
3	UART 0 Receive Data
4	GND

GPIO Header

Pin	V	Pin	GPIO	Function
1	3.3V	2	GPIO11	H_UART_SOUT and EEPROM
3	3.3V	4	GPIO13	AR9344_2G_WLAN_LED and 3.3V/5V control
5	3.3V	6	GPIO16	Buzzer
7	3.3V	8	GPIO18	INTERNET_LED
9	3.3V	10	GPIO19	COM_D
11	3.3V	12	GPIO4	H_UART_SIN and EEPROM

LED Header

Pin	V	Pin	GPIO	Function
1	3.3V	2	-	GND
3	3.3V	4	-	Ethernet0
5	3.3V	6	-	Ethernet1
7	3.3V	8	GPIO15	WAKE_RC/RSS1
9	3.3V	10	GPIO20	RSS2
11	3.3V	12	GPIO21	RSS3
13	3.3V	14	GPIO22	RSS4/Diag

GPIO Pin Mapping

GPIO Pin	Function
GPIO0	TCK
GPIO1	TDI
GPIO2	TDO
GPIO3	TMS
GPIO4	H_UART_SIN and EEPROM
GPIO5	SPI_CS_L
GPIO6	SPI_CLK
GPIO7	SPI_MO_SI
GPIO8	SPI_MI_SO
GPIO9	UART_SIN
GPIO10	UART_SOUT
GPIO11	H_UART_SOUT and EEPROM
GPIO12	GPIO Reset
GPIO13	AR9344_2G_WLAN_LED and 3.3V/5V control
GPIO14	Diag_STATUS_LED
GPIO15	WAKE_RC/RSS1
GPIO16	Buzzer
GPIO17	H_watchdog
GPIO18	INTERNET_LED
GPIO19	COM_D
GPIO20	RSS2
GPIO21	RSS3
GPIO22	RSS4/Diag

LED Arrays

LED Array 1			LED Array 2		
#	Label	Signal	#	Label	Signal
1	DS8	Power	1	J6	Power
2	DS13	Ethernet	2	J2	Ethernet0
3	DS20	RSS1	3	J29	Ethernet1
4	DS21	RSS2	4	J30	RSS1
5	DS22	RSS3	5	J31	RSS2
6	DS19	RSS4/Diag	6	J33	RSS3
			7	J32	RSS4/Diag

SIM Slot Pin Assignment

SIM SLOT 1		SIM SLOT 2		SIM Signal
1	VCC	1	VCC	VCC
2	RST	2	RST	SIM_RST
3	CLK	3	CLK	SIM_CLK
4	CD	4	CD	SIM_CARD_IN
5	GND	5	GND	GND
6	VPP	6	VPP	VPP
7	I/O	7	I/O	SIM_DATA

JTAG Interface Pin Assignment

Pin	Signal	Pin	Signal
1	TRST_N	2	GND
3	TDI	4	GND
5	TDO	6	GND
7	TMS	8	GND
9	TCK	10	GND
11	RESET	12	NC
13	DINT	14	3V3

miniPCle Slot Pin Assignment

Top side		Bottom side	
1	WAKE_L	2	+3.3V
4	NC	4	GND
5	NC	6	NC
7	CLKREQ_L	8	NC
9	GND	10	NC
11	REFCLK-	12	NC
13	REFCLK+	14	NC
15	GND	16	NC
Mechanical key			
17	NC	18	GND
19	NC	20	W_DISABLE_L
21	GND	22	PERST_L
23	PERn0	24	+3.3Vaux
25	PERp0	26	GND
27	GND	28	NC
29	GND	30	NC
31	PETn0	32	NC
33	PETp0	34	GND
35	GND	36	USB_D-
37	NC	38	USB_D+
39	NC	40	GND
41	NC	42	NC
43	NC	44	NC
45	+5V	46	NC
47	+5V	48	NC
49	+5V	50	GND
51	+5V	52	+3.3V