Component Map

UFL connector:
J2, J3
Or MMCX connector:
J4, J5

miniPCle Interface

Power Requirements

DC Power	3.3V for board and 5V for PA
Power Consumption	9.49 Watts (Max)

miniPCle Slot Pin Assignment

Top side		Bottom side	
1	WAKE_L	2	+3.3V
3	CHIP_PWD_L	4	GND
5	NC	6	NC
7	CLKREQ_L	8	NC
9	GND	10	NC
11	REFCLK-	12	NC
13	REFCLK+	14	NC
15	GND	16	NC
Mechanical key			
17	NC	18	GND
19	NC	20	WLAN_DISABLE_N
21	GND	22	PERST_L
23	PERn0	24	+3.3V
25	PERp0	26	GND
27	GND	28	NC
29	GND	30	NC
31	PETn0	32	NC
33	PETp0	34	GND
35	GND	36	NC
37	NC	38	NC
39	NC	40	GND
41	NC	42	NC
43	GND	44	LED_WLAN_L
45	+5V	46	NC
47	+5V	48	NC
49	+5V	50	GND
51	+5V	52	+3.3V

GPIO Pin Mapping

GPIO Function			
GPIO Pin	Function	GPIO Pin	Function
GPIO_17	WLAN_LED	GPIO_19	EEPROM_PROT
GPIO_20	I2C_SDA	GPIO_22	I2C_CLK
GPIO_18	WLAN_DISABLE_N		